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**\*BIBDATASHEET\*****CONFIRMATION NO. 1148**

Bib Data Sheet

SERIAL NUMBER 10/626,532	FILING DATE 07/25/2003  RULE	CLASS 326	GROUP ART UNIT 2819	ATTORNEY DOCKET NO. 500.32647CC8
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**APPLICANTS**

Takeshi Sakata, Kunitachi-shi, JAPAN;

Kiyoo Itoh, Higashikurume-shi, JAPAN;  
 Masashi Horiguchi, Kawasaki-shi, JAPAN;

**\*\* CONTINUING DATA \*\*\*\*\***

This application is a CON of 10/103,966 03/25/2002 PAT 6,621,292  
 which is a DIV of 09/832,853 04/12/2001 PAT 6,384,623  
 which is a CON of 09/573,609 05/19/2000 PAT 6,268,741  
 which is a CON of 09/141,563 08/28/1998 PAT 6,154,062  
 which is a CON of 08/797,051 02/10/1997 PAT 5,825,198  
 which is a CON of 08/620,686 03/21/1996 PAT 5,606,265  
 which is a CON of 08/374,990 01/19/1995 PAT 5,521,527  
 which is a CON of 08/178,020 01/06/1994 PAT 5,408,144

*Jc***\*\* FOREIGN APPLICATIONS \*\*\*\*\***

JAPAN 05-000973 01/07/1993  
 JAPAN 05-015236 02/02/1993

*Jc*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

**\*\* 04/30/2004**

Foreign Priority claimed 35 USC 119 (a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>Paul J. Day</i> <i>Jc</i> Examiner's Signature Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWING 25	TOTAL CLAIMS 9	INDEPENDENT CLAIMS 1
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**ADDRESS**

020457  
 ANTONELLI, TERRY, STOUT & KRAUS, LLP  
 1300 NORTH SEVENTEENTH STREET  
 SUITE 1800  
 ARLINGTON, VA  
 22209-9889

**TITLE**

Semiconductor integrated circuits with power reduction mechanism

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